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APPLICANT

HERBERT et al.

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DHL	5,466,965	11/1995	MENG et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
DHL	0 262 346	04/1988	EP		
DHL	2 002 579	02/1979	GB		
DHL	0 757 392	02/1997	EP		

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

DHL	MISHRA J K et al.: "Design optimization of a Single-sided SI/SIGE heterostructure mixed tunnelling avalanche transit time double drift region" Semiconductor Science and Technology, GB, Institute of Physics, London, vol. 12, no. 12, pp. 1635-1640
DHL	MISHRA J K et al.: "An extremely low noise heterojunction impact" IEEE transactions on electron devices, US, IEEE INC. New York, vol. 44, no. 12, pp. 2143-2148
DHL	KEARNEY M. J.: "Heterojunction impact avalanche transistors diodes grown by molecular beam epitaxy" Semiconductor Science and Technology, GB, Institute of Physics. London, vol. 8, no. 4, pp. 560-567

*Examiner	Donghee Kang	Date Considered	8-21-02
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

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